

AUG. 21. 2002 1:04PM

ON PEABODY

NO. 3109 P. 3

Docket No. 0819-0559

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
Shin HASIMATO et al. ) Attn: Customer Correction Branch  
Serial No. 09/856,823 )  
Filed: May 25, 2001 )  
For: METHOD FOR FABRICATING )  
SEMICONDUCTOR DEVICE )

Attention: Application Processing Division

**REQUEST FOR CORRECTED FILING RECEIPT**

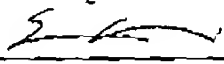
Honorable Commissioner of Patents  
Washington, D.C. 20231

Sir:

The Filing Receipt received in the above-identified application has errors thereon due to Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,

  
Eric J. Robinson  
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NO. 3109 EP. 41 of 3



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FILE FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/856,823	05/29/2001 05/25/2001	1785	1130	0819-559	8	16	2

CONFIRMATION NO. 3480

## FILING RECEIPT



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Nixon Peabody  
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Date Mailed: 07/08/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

### Applicant(s)

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Toru Nishiwaki, Toyama, JAPAN;  
Tomoya Tanaka, Kyoto, JAPAN;

### Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/06851 10/03/2000

### Foreign Applications

JAPAN 11-282668 10/04/1999  
JAPAN 2000-263467 08/31/2000

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

Title

for fabricating  
Method of manufacturing semiconductor device

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NO. 3189 P. 1



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NO. 3109 P. 2

This will acknowledge receipt of the following:

1. Transmittal Form
2. Request for Corrected Filing Receipt
3. Request for Consideration of Documents Cited in International Search Report

In re PATENT application of:

SHIN HASHIMOTO et al  
Serial No. 09/856,823  
Filed: 05/25/2001  
Due Date: 11/25/2002  
For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

Docket: 740819-000559

October 19, 2001

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